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Attorney's Docket No.

COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original. first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF the specification of which: is attached hereto. as Application Serial No. was filed on and was amended on was described and claimed in PCT International Application No. and as amended under PCT Article 19 on I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose all information I know to be material to patentability in accordance with Title 37, Code of Federal Regulations, §1.56. I hereby claim the benefit under Title 35, United States Code, §119(e)(1) of any United States provisional application(s) listed below: Filing Date U.S. Serial No. I hereby claim the benefit under Title 35. United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose all information I know to be material to patentability as defined in Title 37, Code of Federal Regulations, §1.56(a) which became available between the filing date of the prior application and the national or PCT international filing date of this application: U.S. Serial No. I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate or of any PCT international application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed: Application No. **Priority Claimed** Filing Date Country Japan' 2000-280864 September 14, 2000 Yes [] No September 14, 2000 Japan 2000-280902 [X] Yes

John F. Hayden, Reg. No. 37.640: Scott C. Harris, Reg. No. 32,030; Michael McKeon, Reg. No. 37.888; Linda Liu Kordziel, Reg. No. 39,732; William E. Booth, Reg. No. 28,933; Ruffin B. Cordell, Reg. No. 33,487; John W. Freeman, Reg. No. 29,066; Timothy A. French, Reg. No. 30,175; G. Roger Lee, Reg. No. 28,963; Ralph A.

business in the Patent and Trademark Office connected therewith:

I hereby appoint the following attorneys and/or agents to prosecute this application and to transact all

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Combined Declaration and Power of Attorney Page 2 of 2 Pages

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Address all telephone calls to John F. Hayden at telephone number (202) 783-5070

Address all correspondence to John F. Hayden at:

FISH & RICHARDSON P.C. 601 Thirteenth St., N.W. Washington, DC 20005

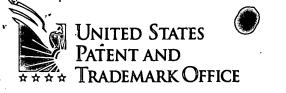
I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patents issued thereon.

Full Name of Inventor: Akira ISHIKAWA

Inventor's Signature:

Residence Address: Kanagawa, Japan Citizenship: Japanese

C/O Semiconductor Energy Laboratory Co., Ltd. Post Office Address: 398, Hase, Atsugi-shi, Kanagawa-ken 243-0036 Japan



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FEBRUARY 13, 2002

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ASSIGNOR:

ISHIKAWA, AKIRA

DOC DATE: 08/23/2001

ASSIGNEE:

SEMICONDUCTOR ENERGY LABORATORY

CO., LTD.

398 HASE ATSUGI-SHI

KANAGAWA-KEN 243-0036, JAPAN

SERIAL NUMBER: 09951384

PATENT NUMBER:

FILING DATE: 09/14/2001

ISSUE DATE:

LAZENA MARTIN, EXAMINER
ASSIGNMENT DIVISION
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12-27-2001

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ASSIGNMENT

For valuable consideration, I, Akira ISHIKAWA of Atsugi, Kanagawa, Japan hereby assign to Semiconductor Energy Laboratory Co., Ltd., a Japanese corporation having a place of business at:

398, Hase, Atsugi-shi, Kanagawa-ken 243-0036 Japan

and its successors and assigns (collectively hereinafter called "the Assignee"), the entire right, title and interest throughout the world in the inventions and improvements which are subject of an application for United States Patent signed by us this day, entitled

Them signed by as this day, entitled
SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF
filed, and assigned U.S. Serial Number, and we authorize
and request the attorneys appointed in said application to hereafter complete this assignment by inserting above the
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assigns, without further compensation to perform such lawful acts and to sign such further applications, assignments
Preliminary Statements and other lawful documents as the Assignee may reasonably request to effectuate fully this
assignment.

Signature: Akira Ishikawa

Name: Akira ISHIKAWA

Date: 08/23/2001